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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial NoAPR. 2.0. 2002	
Filing Date	<u>April 12, 2001</u>
Inventor	Luan C. Tran-
Assignee RADEMAN	Luan C. Tran- Micron Technology, Inc.
Group Art Unit	
Examiner	L. Schillinger
Attorney's Docket No	Ml22-1637
Title: Semiconductor Processing Meth	ods Of Forming Transistors, Semiconductor
Processing Methods Of Forming Dynamic Random Access Memory Circuitry, And	
Related Integrated Circuitry	

RESPONSE TO JANUARY 30, 2002 OFFICE ACTION

To:

Box Fee Amendment

Assistant Commissioner for Patents

Washington, D.C. 20231

From:

Frederick M. Fliegel, Ph.D.

Tel. 509-624-4276; Fax 509-838-3424

Wells St. John P.S.

601 W. First Avenue, Suite 1300

Spokane, WA 99201-3817

⊹Sir:⊬

Responsive to the Office Action dated January 30, 2002, Applicant amends and remarks as follows:

AMENDMENTS

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